

## CLAIMS

What is claimed is:

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1. ~~A method of displaying an image comprising the steps of:~~  
providing a liquid crystal display having a plurality of pixel electrodes;  
writing an image to the display such that the liquid crystal moves to an image position;  
flashing a light source to illuminate the display;  
setting the pixel electrodes to orient the liquid crystal to a second position; and  
repeating the writing, flashing, and setting steps to produce a sequence of images.
  2. The method of claim 1 wherein the image is a color image and the writing of the image is associated with color light that is flashed after the writing and the steps of writing, flashing, and setting are repeated for a plurality of colors.
  3. The method of claim 2 wherein the liquid crystal display is an active matrix display having at least 75,000 pixel electrodes and having an active area of less than 160 mm<sup>2</sup>.
  4. The method of claim 3 wherein the liquid crystal display is transmissive and the light source is a backlight that illuminates through the display.
  5. The method of claim 4 wherein the light source has at least one light emitting diode (LED) and the LED intensity is varied dependent on ambient light.

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6. The method of claim 4 comprising the step of switching the voltage of the counterelectrode after each flashing of the light source and prior to the next writing of the image.
7. The method of claim 6 wherein the writing of the image to the display by setting the voltage to each pixel electrode is done sequentially starting at one corner and progressing until ending in the opposite corner.
8. The method of claim 7 wherein the writing of the image is started at an upper corner in a subframe and the image is written to the display starting at a lower corner on the next subframe, and the process of writing the image continues to alternate between starting at an upper corner and a lower corner for subsequent subframes.

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7. The method of claim 6 wherein the writing of the image to the display by setting the voltage to each pixel electrode is done sequentially starting at one corner and progressing until ending in the opposite corner.

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8. The method of claim 7 wherein the writing of the image is started at an upper corner in a subframe and the image is written to the display starting at a lower corner on the next subframe, and the process of writing the image continues to alternate between starting at an upper corner and a lower corner for subsequent subframes.

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9. ~~The method of claim 7 further comprising the step of waiting a settling time to allow the liquid crystal to twist between the writing of the last pixel and the flashing of the light source.~~
10. The method of claim 9 wherein the liquid crystal is driven black and relaxes clear and the setting the pixel electrodes to a specific value to initialize the display are set to a value to relax the liquid crystal towards clear.

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10. The method of claim 9 wherein the liquid crystal is driven black and relaxes clear and the setting the pixel electrodes to a specific value to initialize the display are set to a value to relax the liquid crystal towards clear.

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11. The method of claim 3 wherein the step of writing an image to the display is accomplished by writing to one pixel electrode at a time.
- 20 12. The method of claim 3 wherein the step of writing an image to the display is accomplished by writing to a plurality of pixel electrodes simultaneously.

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12. The method of claim 3 wherein the step of writing an image to the display is accomplished by writing to a plurality of pixel electrodes simultaneously.

13. ~~The method of claim 3 further comprising the step of monitor the power to the microdisplay and initializing a process to discharge the storage capacitor of the pixels to zero when the power drops below a certain level to the display.~~
14. A method of displaying an image comprising the steps of:
- 5       providing a liquid crystal display having a plurality of pixel electrodes;  
      writing an image to the display therein causing the liquid crystal to move to a specific image position;  
      flashing a light source to illuminate the display;  
      switching the voltage of the counterelectrode;
- 10       setting the pixel electrodes to a specific value to cause the liquid crystal to move towards a desired position; and  
      repeating the writing, flashing, switching, and setting to produce an image.
15. The method of displaying an image of claim 14 wherein the liquid crystal display is an active matrix display having at least 75,000 pixel electrodes and having an active area of less than 160 mm<sup>2</sup>.
16. The method of claim 15 wherein the light source includes at least one light emitting diode (LED).
17. The method of claim 16 wherein the light crystal display is transmissive and the light source is a backlight that illuminates through the display.
- 20       18. The method of claim 17 wherein the image is a multi-color image and the writing of the image is associated with a color light source that is flashed after the writing of the image, and the process is repeated for each of the different color light sources.

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19. The method of claim 17 wherein the light source intensity is varied dependent on ambient light.

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20. ~~The method of claim 16 further comprising the step of monitor the power to the microdisplay and initializing a process to discharge the storage capacitor of the pixels to zero when the power drops below a certain level to the display.~~

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- (2) 21. An active matrix liquid crystal display comprising:  
 an active matrix circuit having an array of transistor circuits formed in a first plane, each transistor circuit being connected to a pixel electrode in an array of pixel electrodes having an area of 200 mm<sup>2</sup> or less;  
 an integrated circuit display controller connected to the active matrix circuit, the controller including a read memory, a write memory and a timing control circuit;  
 a counterelectrode panel extending in a second plane that is parallel to the first plane, such that the counterelectrode panel receives an applied voltage;  
 and  
 a liquid crystal layer interposed in a cavity between the two planes.

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22. The active matrix liquid crystal display of claim 21 further comprising circuitry for setting voltage of the pixel electrodes to the voltage of the counterelectrode to initialize the display at each subframe.

- 20 23. The active matrix liquid crystal display of claim 22 further comprising circuitry to heat the liquid crystal display.

24. The active matrix liquid crystal display of claim 23 further comprising a sensor interposed between the substrates to monitor a property of the liquid crystal.

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The active matrix liquid crystal display of claim 24 wherein the writing of the image to the display by setting the voltage to each pixel electrode is done sequentially starting at one corner and progressing until ending in the opposite corner.

5 26. The active matrix liquid crystal display of claim 25 wherein the writing of the image is started at an upper corner in a subframe and the image is written to the display starting at a lower corner on the next subframe, and the process of writing the image continues to alternate between starting at an upper corner and a lower corner for subsequent subframes.

10 27. The active matrix liquid crystal display of claim 25 wherein the property that is measured is the temperature of the liquid crystal.

28. The active matrix liquid crystal display of claim 25 wherein the property that is measured is the capacitance of the liquid crystal.

29. The active matrix liquid crystal display of claim 28 wherein the array of  
15 transistor circuits are formed on an oxide layer and the oxide layer is thinned at  
the pixel electrodes.

30. The active matrix liquid crystal display of claim 29 wherein the oxide layer is thinned adjacent to the liquid crystal.

31. The active matrix liquid/crystal display of claim 29 wherein the oxide layer is  
20 thinned to form a depression to receive the pixel electrode.

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A microdisplay system comprising:

an active matrix liquid crystal display including an array of pixel electrodes;

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a display circuit having a pair of memory elements and at least one controller that controls writing and reading from the memory elements such that a first memory element is being written while the data from a second memory element is sent to the display;

a light source that illuminates the array of pixel electrodes; and

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a lens that magnifies an image formed on the active matrix liquid crystal display.

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The microdisplay system of claim 32 further comprising a multiplexer for directing the signal and output process for inverting selected video signals.

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The microdisplay system of claim 32 further comprising at least one scaling circuit for interpolating image data from a certain number of pixel data to a preferred number of pixel data for the display.

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The microdisplay system of claim 34 wherein the scaling is of horizontal lines of video data.

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The microdisplay system of claim 34 wherein the scaling is of vertical columns of video data and further comprising a buffer for storing data.

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The microdisplay system of claim 34 further comprising a gamma correction circuit for converting an input signal to an output signal which results in proper intensity on the display.

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The microdisplay system of claim 37 wherein the gamma correction circuit has

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39. The microdisplay system of claim 37 wherein the gamma correction circuit has a look up table for converting an input signal to an output signal which results in proper intensity on the display
40. The microdisplay system of claim 39 further comprising circuitry for setting  
5 voltage of the pixel electrodes to the voltage of the counterelectrode to initialize the display at each subframe and a circuit for switching the voltage of the counterelectrode..
41. The microdisplay system of claim 39 further comprising reordering the values of the data for increase efficient use of memory.
- 10 42. The microdisplay system of claim 32 further comprising circuitry for setting voltage of the pixel electrodes to the voltage of the counterelectrode to initialize the display at each subframe.
43. The microdisplay system of claim 42 further comprising a circuit for switching the voltage of the counterelectrode.
- 15 44. The microdisplay system of claim 43 further comprising a gamma correction circuit having a look up table for converting an input signal to an output signal which results in proper intensity on the display.
45. The microdisplay system of claim 44 further comprising reordering the values of the data for increase efficient use of memory.
- 20 46. The microdisplay system of claim 44 further comprising at least one scaling circuit for interpolating image data from a certain number of pixel data to a preferred number of pixel data for the display.

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47. The microdisplay system of claim 42 further comprising a digital table for converting an input video signal to a corrected output value to achieve proper twist of the liquid crystal to have proper intensity.
48. The microdisplay system of claim 42 further comprising a pair of switching circuits for pseudo-random one of a plurality of signals through one of a plurality of amplifiers and to the display to balance the relative strength of the plurality of signals.
49. An analog video system comprising:
- a restorer that restores a black level of a video signal from a composite signal;
  - a filter for separating a synchronization signal from the composite signal;
  - an active matrix liquid crystal display for receiving the video signal including:
    - an active matrix circuit having an array of transistor circuits formed in a first plane, each transistor circuit being connected to a pixel electrode in an array of pixel electrodes having an area of  $200 \text{ mm}^2$  or less;
    - a counterelectrode panel extending in a second plane that is parallel to the first plane, such that the counterelectrode panel receives an applied voltage; and
    - a liquid crystal layer interposed in a cavity between the two planes, the cavity having a depth of less than 3 microns; and
    - a timing control circuitry that controls the display and receiving the synchronization signal; and
    - a light source that illuminates the array of pixel electrodes.



50. The analog video system of claim 49 further comprising a gamma corrector having a pair of diodes selected based on characteristics of the liquid crystal and a stabilization offset ground circuitry having a linear diode to adjust a center point of a gamma correction curve.
- 5 51. The analog video system of claim 49 wherein the active matrix liquid crystal display further comprises a delay lock loop in a clock signal path, the delay - locked loop having a voltage-controlled delay element in the clock signal path and a feed back loop with a phase detector and an integrator for controlling the voltage-control delay.
- 10 52. The analog video system of claim 49 wherein the active matrix liquid crystal display further comprises a phase-locked loop in a clock signal path, the phase-locked loop has a voltage-controlled oscillator for generating an internal clock signal and a feed back loop with a phase detector and an integrator for controlling the phased-locked loop.
- 15 53. The analog video system of claim 49 further comprising a digital logic circuit for detecting vertical synchronization signals, the digital logic circuit including a counter for receiving a clock signal and synchronization signal and counting clock signal when the synchronization signal is a specific state, a pair of flip flops for sending a signal if the clock sign, a pair of flip flops each being set upon a specific pattern and timing of vertical synchronization signals being detected, and a vertical counter.
- 20 54. The analog video system of claim 49 further comprising circuitry to switch the voltage of the counterelectrode after each subframe.

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55. The analog video system of claim 54 further comprising circuitry for setting voltage of the pixel electrodes to the voltage of the counterelectrode to initialize the display.
56. The analog video system of claim 55 wherein the circuitry to switch the voltage of the counterelectrode occurs in the subframe between the end of the writing to the pixel electrodes and the beginning of the next subframe.
57. A video recording system comprising:
- an image sensor for gathering data from an image and generating a composite signal;
  - a recording device for recording the composite signal;
  - an active matrix liquid crystal display for receiving a video signal including:
    - an active matrix circuit having an array of transistor circuits formed in a first plane, each transistor circuit being connected to a pixel electrode in an array of pixel electrodes having an area of  $200 \text{ mm}^2$  or less;
    - a counterelectrode panel extending in a second plane that is parallel to the first plane, such that the counterelectrode panel receives an applied voltage; and
    - a liquid crystal layer interposed in a cavity between the two planes, the cavity having a depth of less than 3 microns; and
    - a processor circuit for routing the composite signal between the image sensor, the recording device and the display;
    - a dc restorer for restoring black level of the video signal from the composite signal;
    - a filter for separating a synchronization signal from the composite signal;

a timing control circuitry for controlling the display and receiving the synchronization signal; and

a light source that illuminates the array of pixel electrodes.

58. The video recording system of claim 57 further comprising circuitry to switch  
5 the voltage of the counterelectrode after each subframe.
59. The video recording system of claim 58 further comprising a gamma corrector  
having a pair of diodes selected based on characteristics of the liquid crystal and  
a stabilization offset ground circuitry having a linear diode to adjust a center  
point of a gamma correction curve.
- 10 60. The video recording system of claim 59 wherein the circuitry to switch the  
voltage of the counterelectrode occurs in the subframe between the end of the  
writing to the pixel electrodes and the beginning of the next subframe.
61. The video recording system of claim 59 wherein the active matrix liquid crystal  
display further comprises a delay lock loop in a clock signal path, the delay -  
15 locked loop having a voltage-controlled delay element in the clock signal path  
and a feed back loop with a phase detector and an integrator for controlling the  
voltage-control delay.
62. The video recording system of claim 59 wherein the active matrix liquid crystal  
display further comprises a phase-locked loop in a clock signal path, the  
20 phase-locked loop has a voltage-controlled oscillator for generating an internal  
clock signal and a feed back loop with a phase detector and an integrator for  
controlling the phased-locked loop.

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63. The analog video system of claim 59 further comprising a view finder housing for carrying the microdisplay, the light source, the timing control circuit and a lens for magnifying the image by at least a factor of two.
64. The analog video system of claim 63 wherein the view finder housing has a volume of less than 100 cm<sup>3</sup>.
65. A digital camera comprising:
- a charge coupled device (CCD) for recording an image;
  - an active matrix liquid crystal display including:
    - an active matrix circuit having an array of transistor circuits formed in a first plane, each transistor circuit being connected to a pixel electrode in an array of pixel electrodes having an area of less than 200 mm<sup>2</sup>;
    - a counterelectrode panel extending in a second plane that is parallel to the first plane, such that the counterelectrode panel receives an applied voltage; and
    - a liquid crystal layer interposed in a cavity between the two planes, the cavity having a depth of less than 3 microns; and
  - timing control circuit for controlling the CCD and the active matrix liquid display;
  - circuitry for setting voltage of the pixel electrodes to the voltage of the counterelectrode to initialize the display;
  - a memory card for storing image data;
  - a light source that illuminates the array of pixel electrodes; and
  - a lens positioned to receive an image formed on the active matrix liquid crystal display and magnifies the image by at least a factor of two.

66. The analog video system of claim 65 further comprising circuitry to switch the voltage of the counterelectrode after each subframe.

67. The analog video system of claim 66 further comprising a gamma correction circuit for converting an input signal to an output signal which results in proper intensity on the display.

68. A portable communication system comprising:

a wireless transceiver;

an active matrix liquid crystal display including:

an active matrix circuit having an array of transistor circuits formed in a first plane, each transistor circuit being connected to a pixel electrode in an array of pixel electrodes having an area of  $200 \text{ mm}^2$  or less;

a counterelectrode panel extending in a second plane that is parallel to the first plane, such that the counterelectrode panel receives an applied voltage; and

a liquid crystal layer interposed in a cavity between the two planes, the cavity having a depth of less than 3 microns; and

a light source that illuminates the array of pixel electrodes;

a lens positioned to receive an image formed on the active matrix liquid crystal display and magnifies the image by at least a factor two; and  
circuitry for setting voltage of the pixel electrodes to the voltage of the counterelectrode to initialize the display.

69. The portable communication system of claim 68 further comprising a cellular telephone.

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70. The portable communication system of claim 89 further comprising circuitry to switch the voltage of the counterelectrode after each subframe and a gamma correction circuit for converting an input signal to an output signal which results in proper intensity on the display.
- 5 71. A digital printer comprising:  
a control circuit for taking an electronic image and manipulating the image;  
an active matrix liquid crystal display for receiving the tailored image from the control circuit;  
10 a backlight that illuminates the liquid crystal display.
72. The digital printer of claim 71 further comprising a lens for focusing the image of the display on a photographic plane.
73. The digital printer of claim 71 wherein the active matrix display is a color sequential display system and the backlight is a three color light emitting diodes (LEDs) backlight.  
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74. The digital printer of claim 73 further comprising a photographic film positioned in the photographic plane.
75. The digital printer of Claim 71 wherein the array of pixel electrodes comprises an array of at least 320 x 240 and having an active area of less than 160 mm<sup>2</sup>.
- 20 76. The digital printer of Claim 75 wherein the array of pixel electrodes comprises an array of at least 640 x 480.

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- 5 77. The digital printer of claim 75 further comprising a lens for focusing the image of the display on a photographic plane.
78. The digital printer of claim 77 wherein the backlight is a light emitting diode (LED).
- 10 79. An instant camera comprising  
a charge coupled device (CCD) for recording an image;  
a control circuit for taking the electronic image from the CCD and  
manipulating the image;  
an active matrix liquid crystal display for receiving the manipulated  
15 image;  
a light emitting diode (LED) device that illuminates the liquid crystal display;  
a photographic plane; and  
a lens for focusing the image of the liquid crystal display on the  
photographic plane.
- 20 80. The instant camera of claim 79 wherein the array of pixel electrodes comprises an array of at least 320 x 240 and having an active area of less than 160 mm<sup>2</sup>.
81. The instant camera of claim 80 wherein the array of pixel electrodes comprises an array of at least 640 x 480.
- 20 82. The instant camera of claim 79 wherein the housing has a volume of less than 1000 cm<sup>3</sup>.
83. The instant camera of claim 82 wherein the housing has a volume of less than 750 cm<sup>3</sup>.

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85. A method of producing an print comprising the steps of:  
 providing a digital image in a  
 splitting the digital image into  
 driving an active matrix liquid crystal display to  
 projecting a light through the liquid crystal display to project the image  
 of the liquid crystal display on a photosensitive paper; and  
 repeating the process for the other colors.

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1. The first part of the document is a list of names and their corresponding addresses. The names are listed in a column on the left, and the addresses are listed in a column on the right. The names are: John A. Smith, John B. Smith, John C. Smith, John D. Smith, John E. Smith, John F. Smith, John G. Smith, John H. Smith, John I. Smith, John J. Smith, John K. Smith, John L. Smith, John M. Smith, John N. Smith, John O. Smith, John P. Smith, John Q. Smith, John R. Smith, John S. Smith, John T. Smith, John U. Smith, John V. Smith, John W. Smith, John X. Smith, John Y. Smith, John Z. Smith. The addresses are: 123 Main St., 456 Main St., 789 Main St., 101 Main St., 202 Main St., 303 Main St., 404 Main St., 505 Main St., 606 Main St., 707 Main St., 808 Main St., 909 Main St., 1010 Main St., 1111 Main St., 1212 Main St., 1313 Main St., 1414 Main St., 1515 Main St., 1616 Main St., 1717 Main St., 1818 Main St., 1919 Main St., 2020 Main St., 2121 Main St., 2222 Main St., 2323 Main St., 2424 Main St., 2525 Main St., 2626 Main St., 2727 Main St., 2828 Main St., 2929 Main St., 3030 Main St., 3131 Main St., 3232 Main St., 3333 Main St., 3434 Main St., 3535 Main St., 3636 Main St., 3737 Main St., 3838 Main St., 3939 Main St., 4040 Main St., 4141 Main St., 4242 Main St., 4343 Main St., 4444 Main St., 4545 Main St., 4646 Main St., 4747 Main St., 4848 Main St., 4949 Main St., 5050 Main St., 5151 Main St., 5252 Main St., 5353 Main St., 5454 Main St., 5555 Main St., 5656 Main St., 5757 Main St., 5858 Main St., 5959 Main St., 6060 Main St., 6161 Main St., 6262 Main St., 6363 Main St., 6464 Main St., 6565 Main St., 6666 Main St., 6767 Main St., 6868 Main St., 6969 Main St., 7070 Main St., 7171 Main St., 7272 Main St., 7373 Main St., 7474 Main St., 7575 Main St., 7676 Main St., 7777 Main St., 7878 Main St., 7979 Main St., 8080 Main St., 8181 Main St., 8282 Main St., 8383 Main St., 8484 Main St., 8585 Main St., 8686 Main St., 8787 Main St., 8888 Main St., 8989 Main St., 9090 Main St., 9191 Main St., 9292 Main St., 9393 Main St., 9494 Main St., 9595 Main St., 9696 Main St., 9797 Main St., 9898 Main St., 9999 Main St.